

FIG. 1

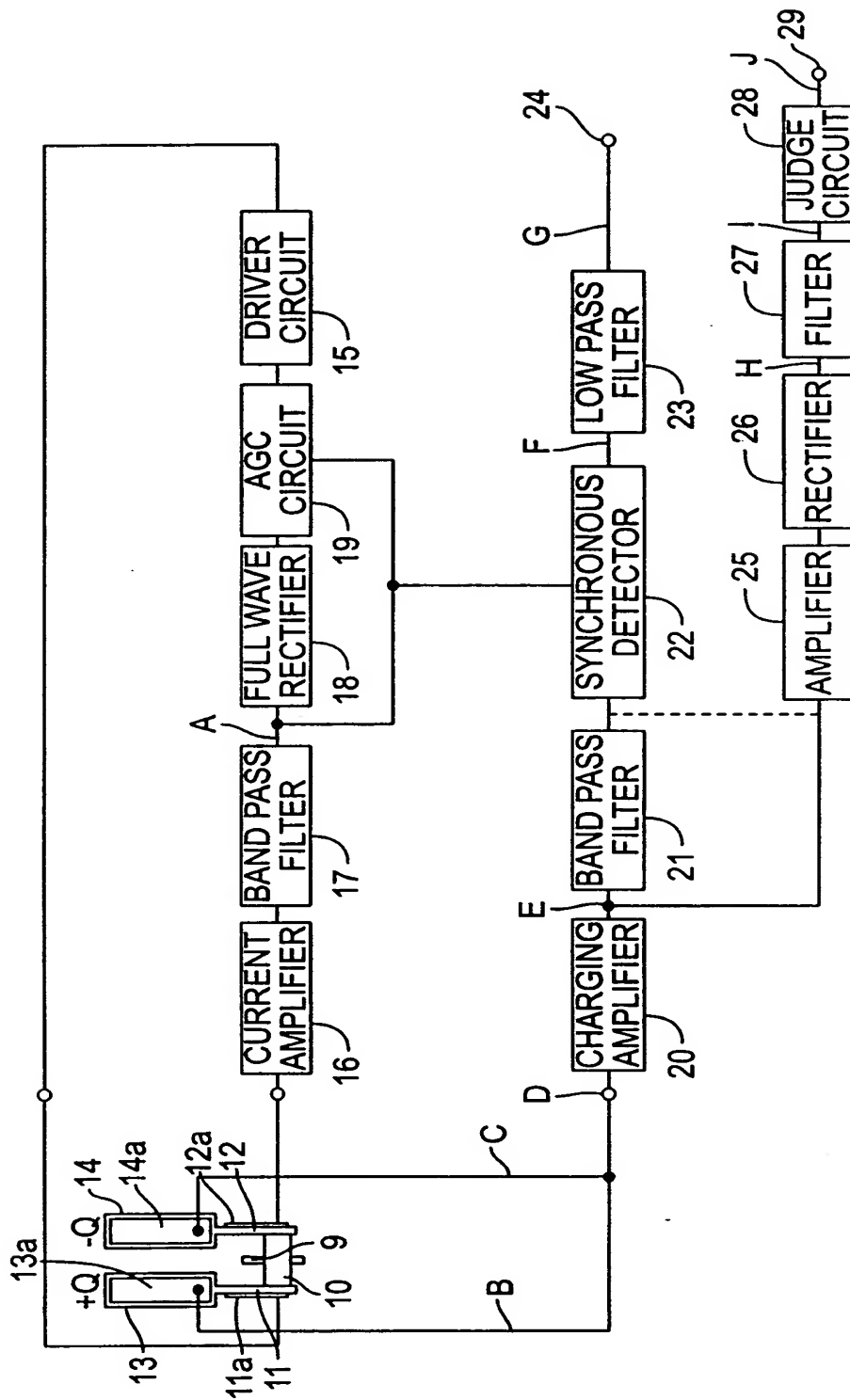


FIG. 2

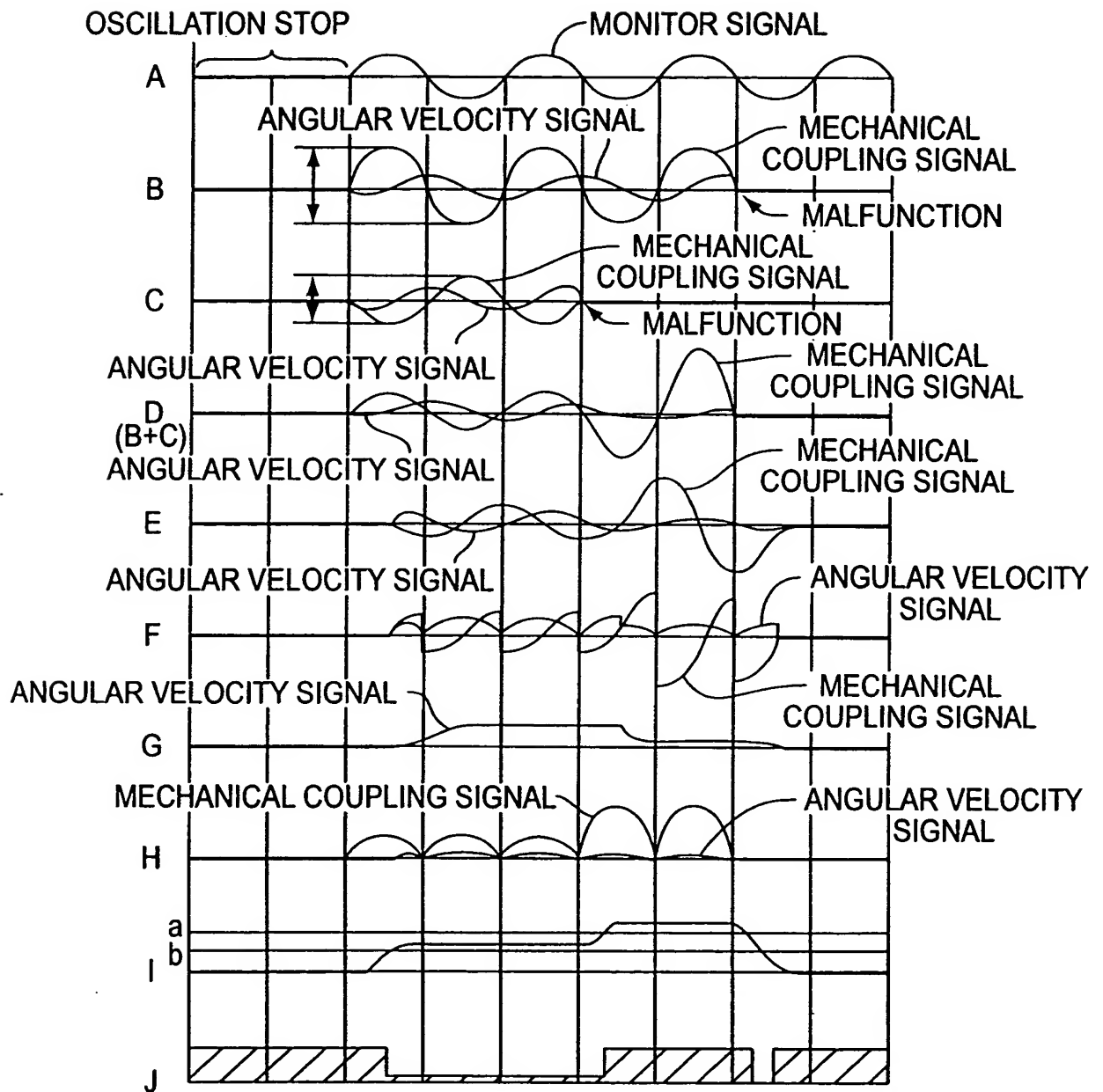


FIG. 3

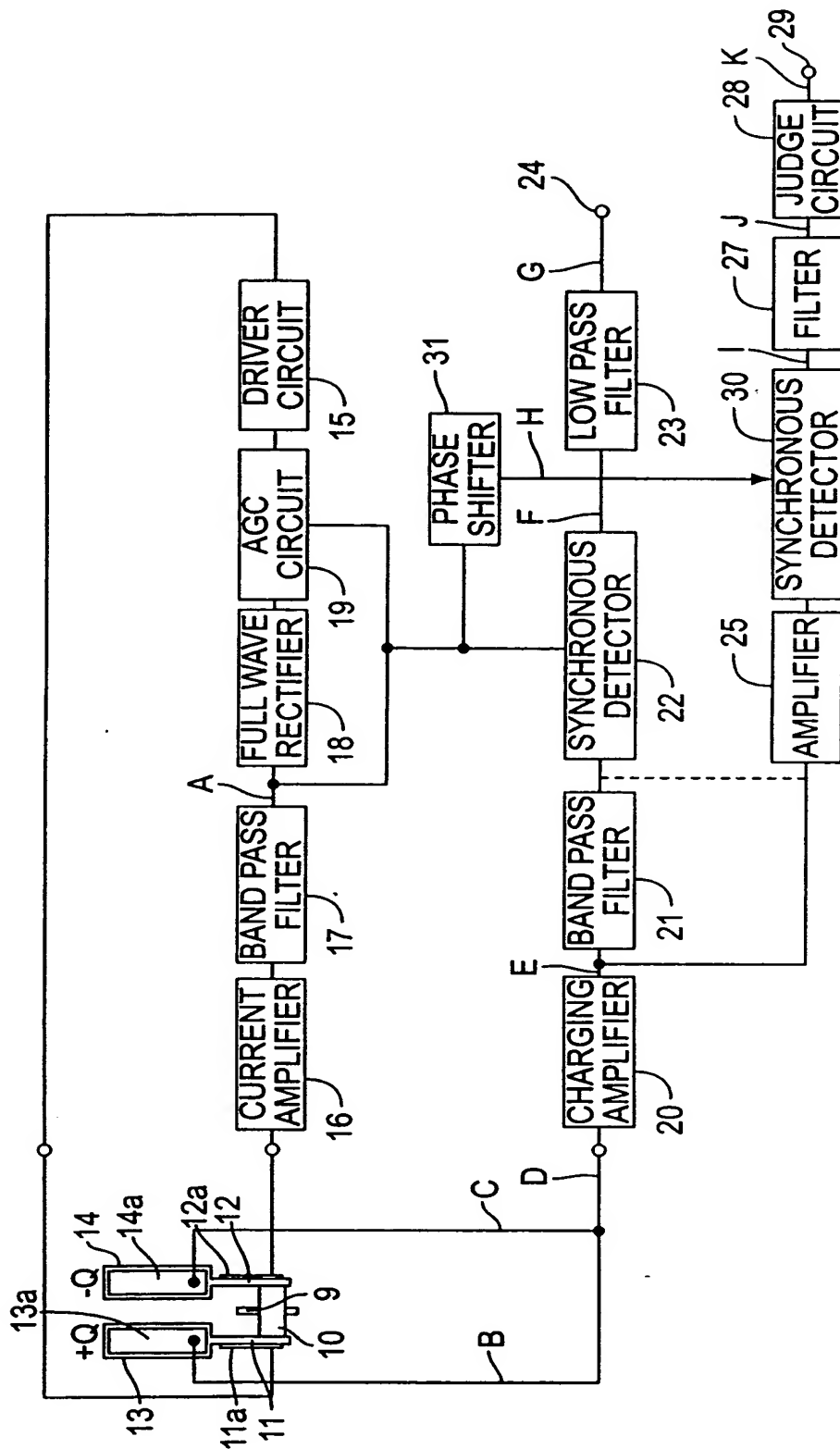


FIG. 4

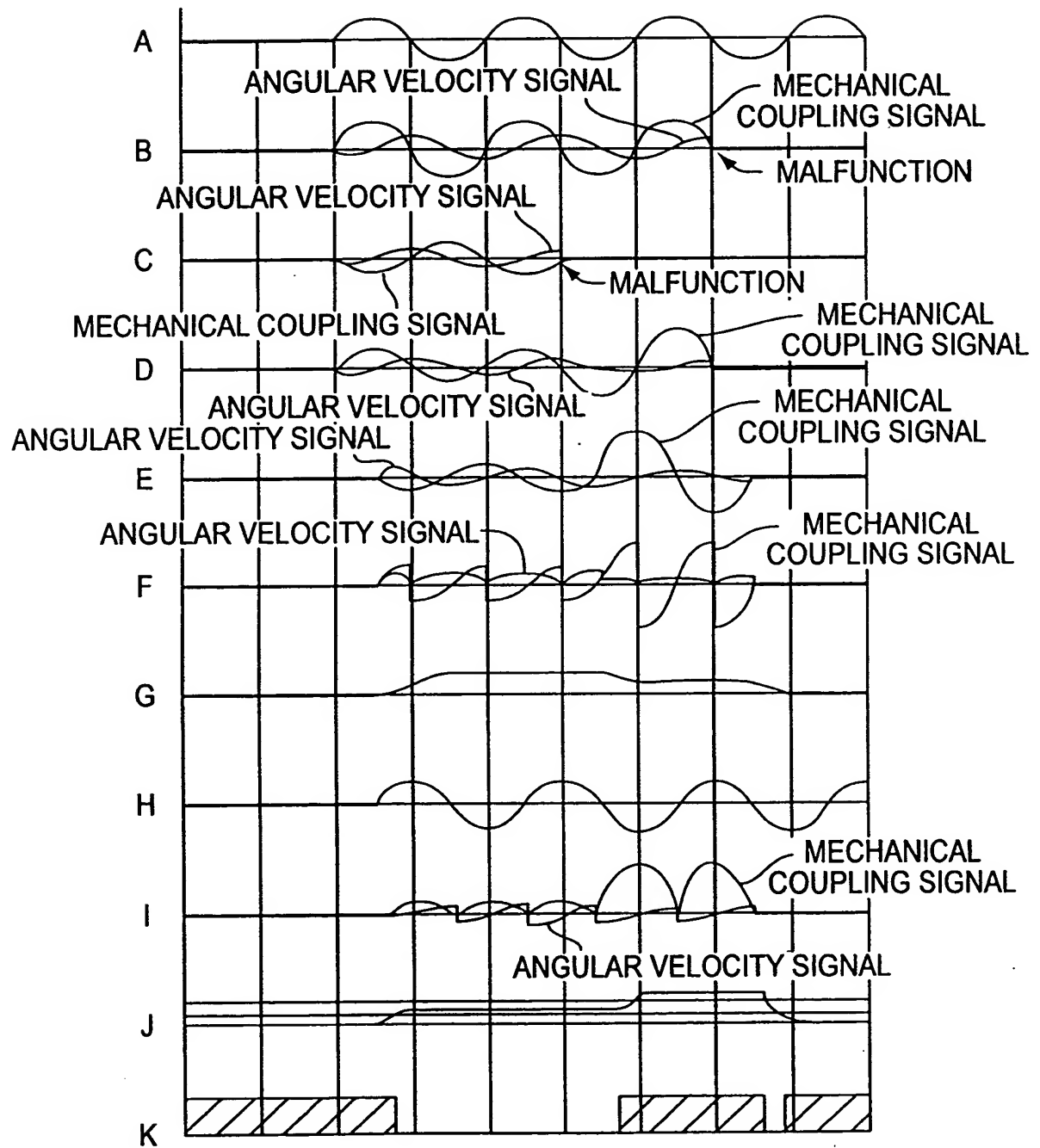


FIG. 5

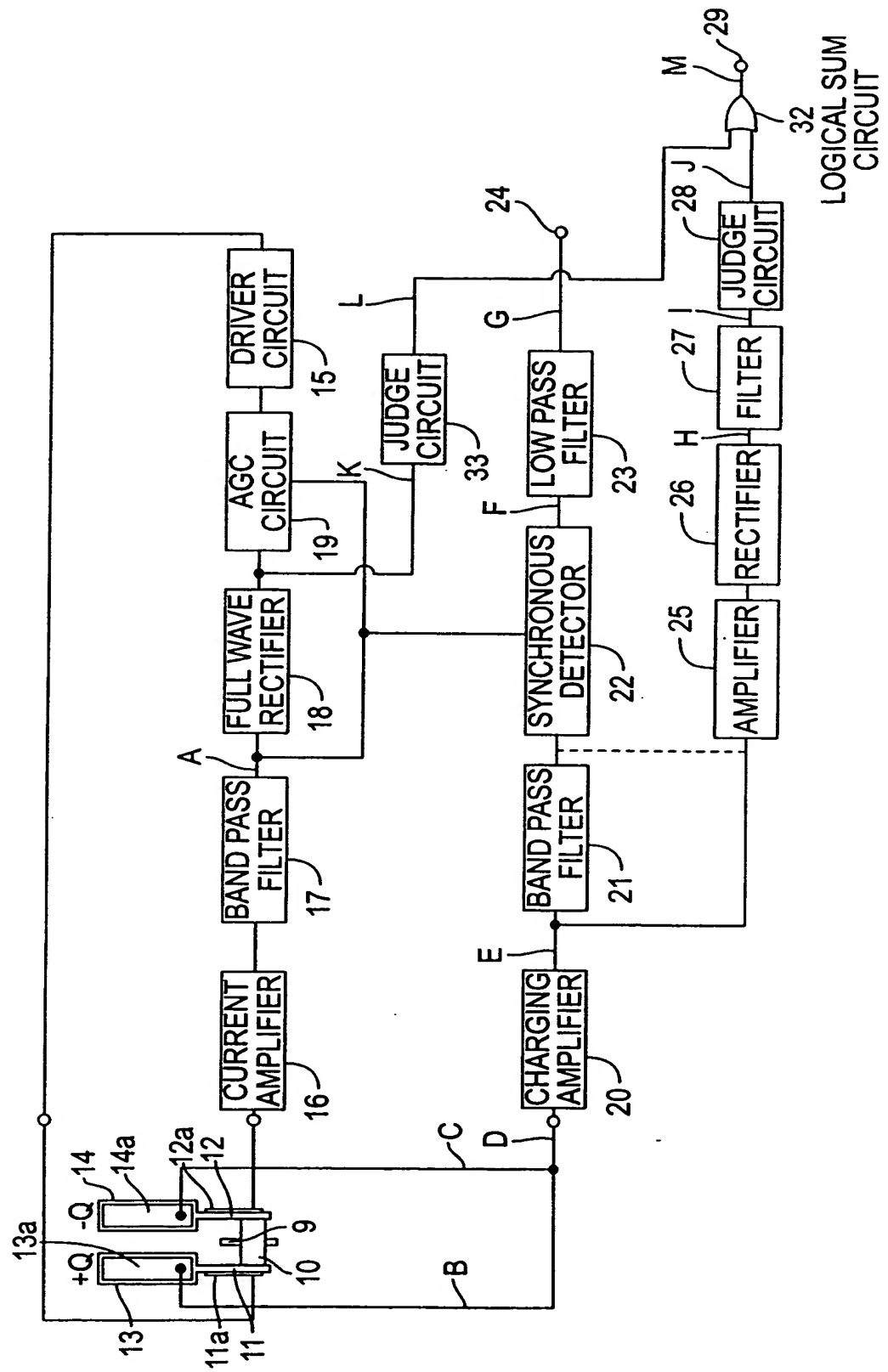


FIG. 6

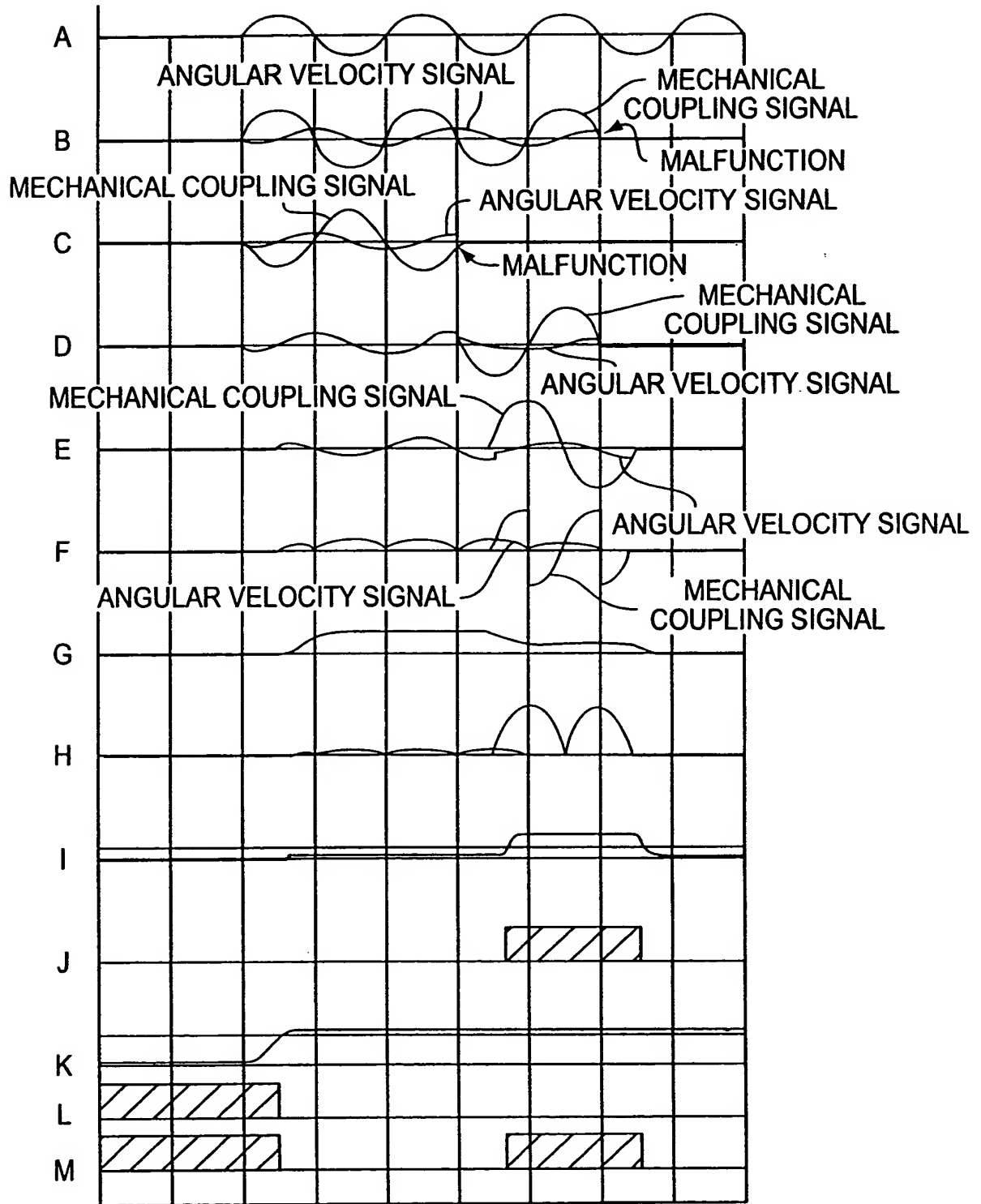


FIG. 7

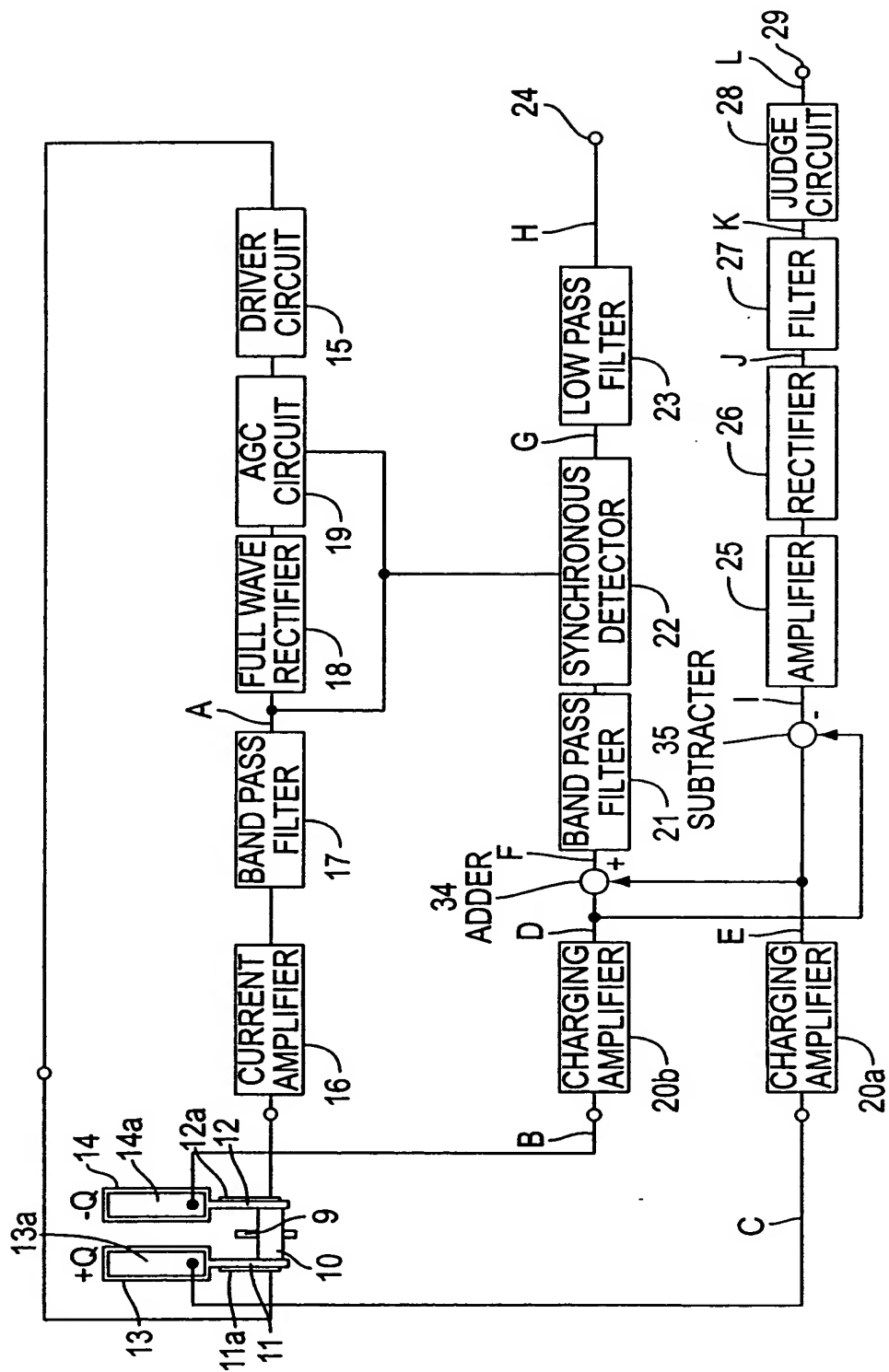


FIG. 8

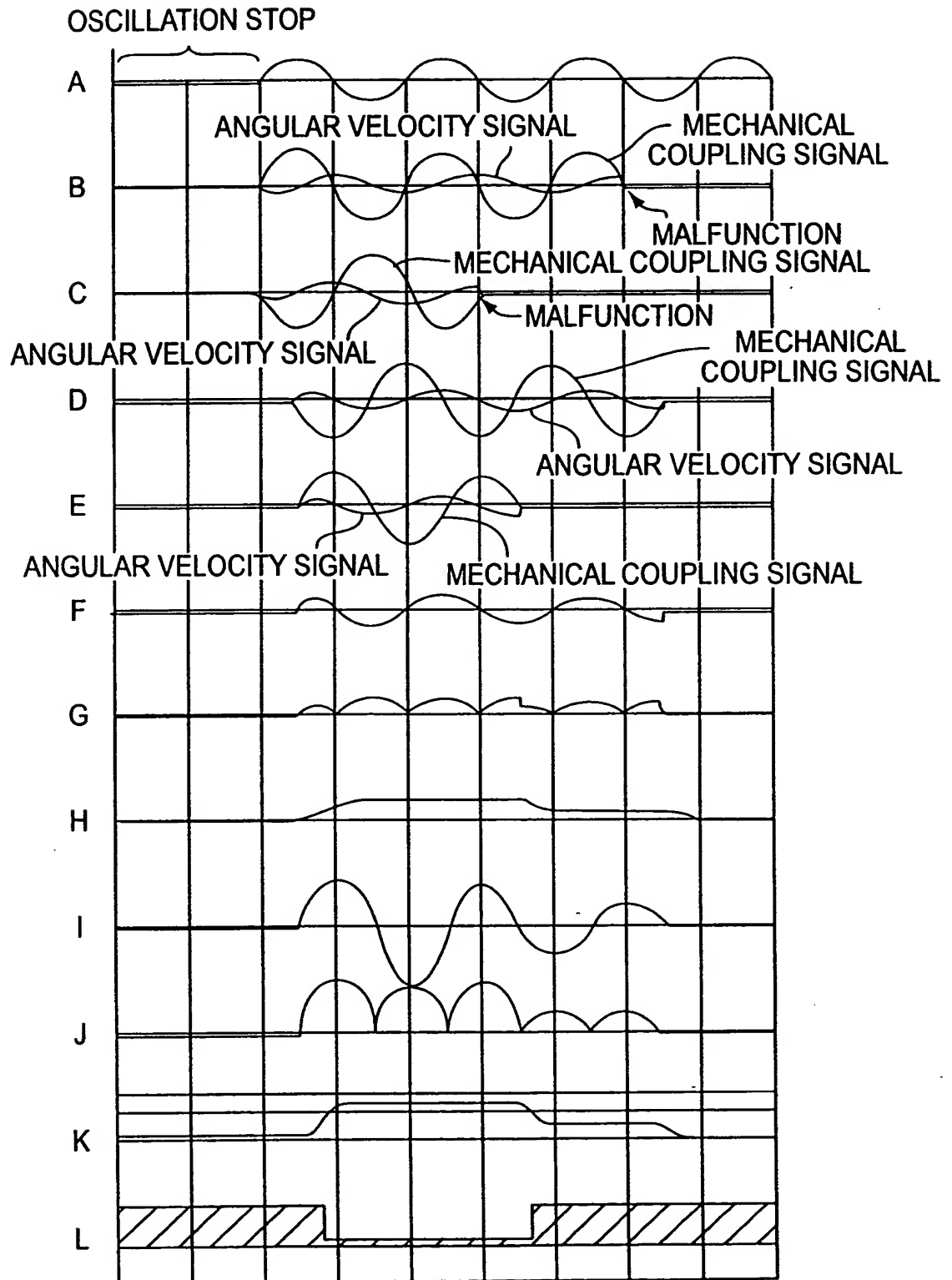


FIG. 9

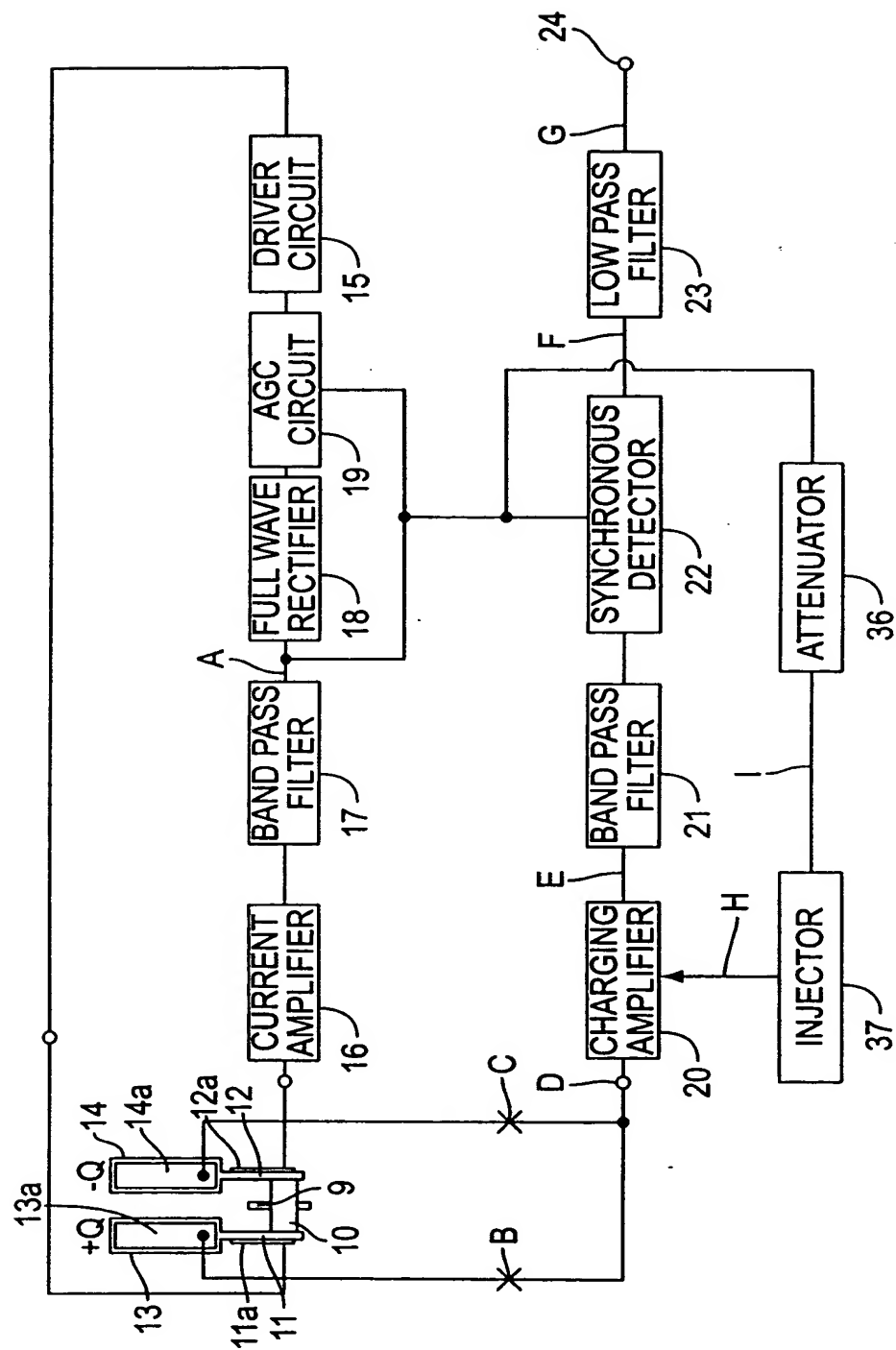


FIG. 10

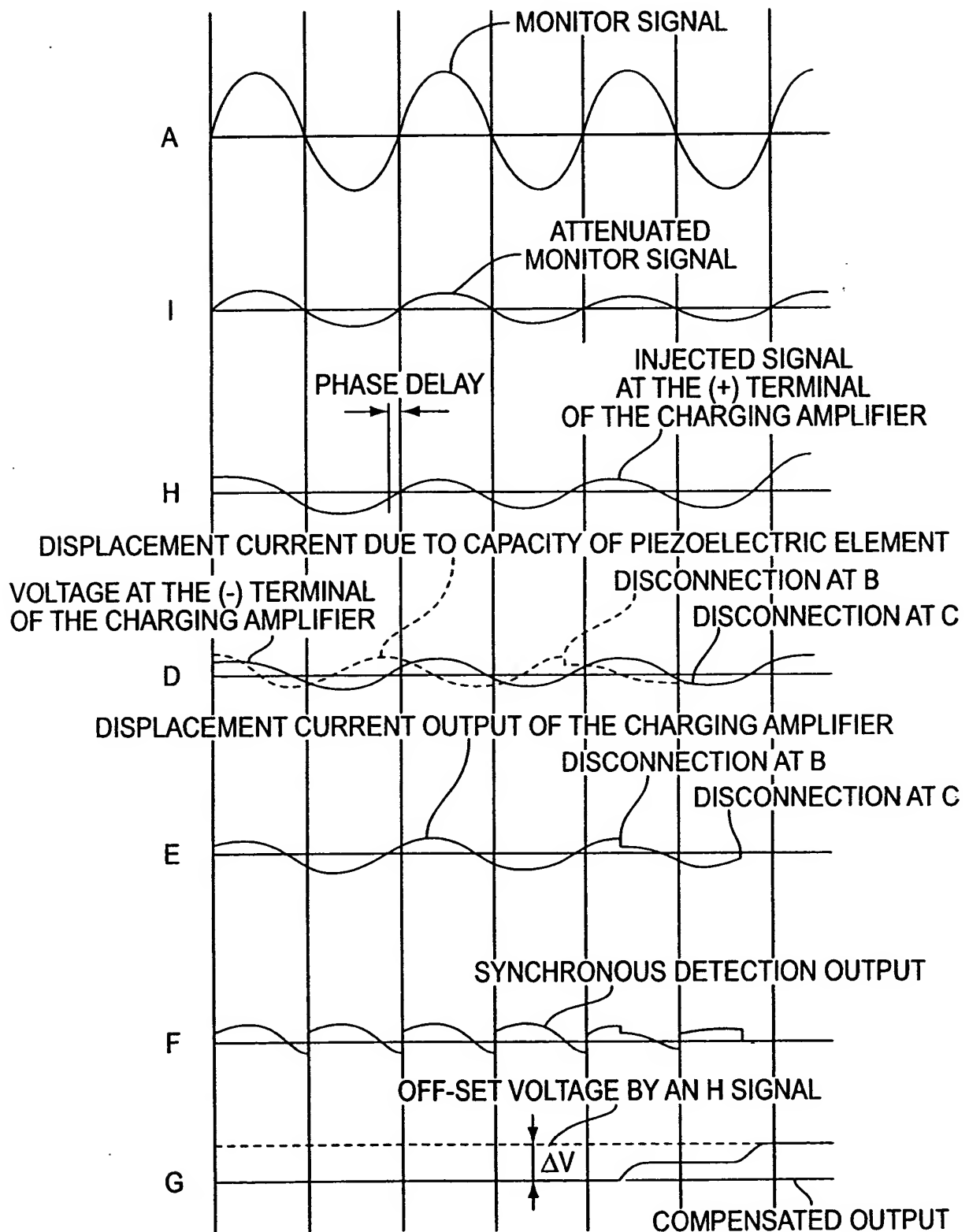


FIG. 11A

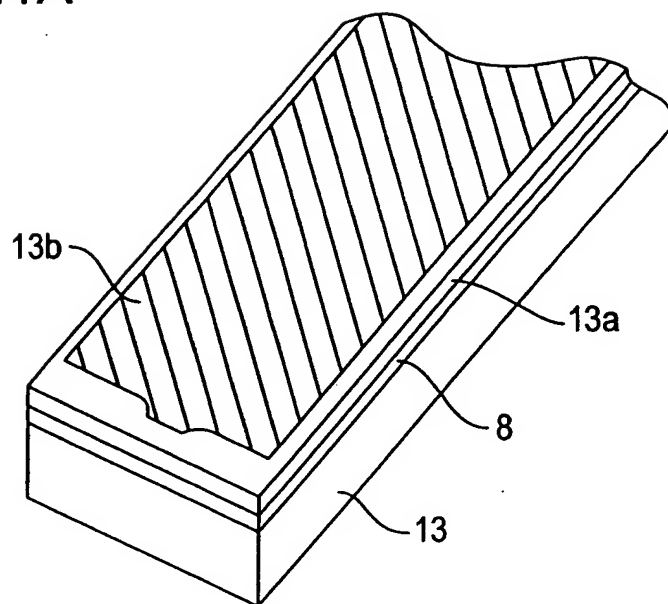


FIG. 11B

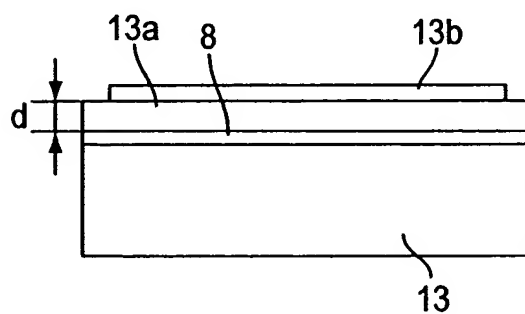


FIG. 11C

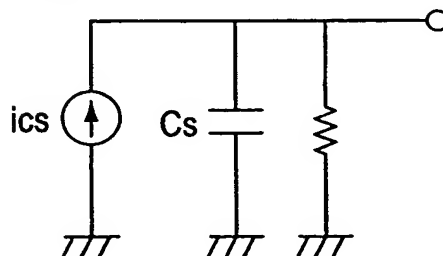


FIG. 12

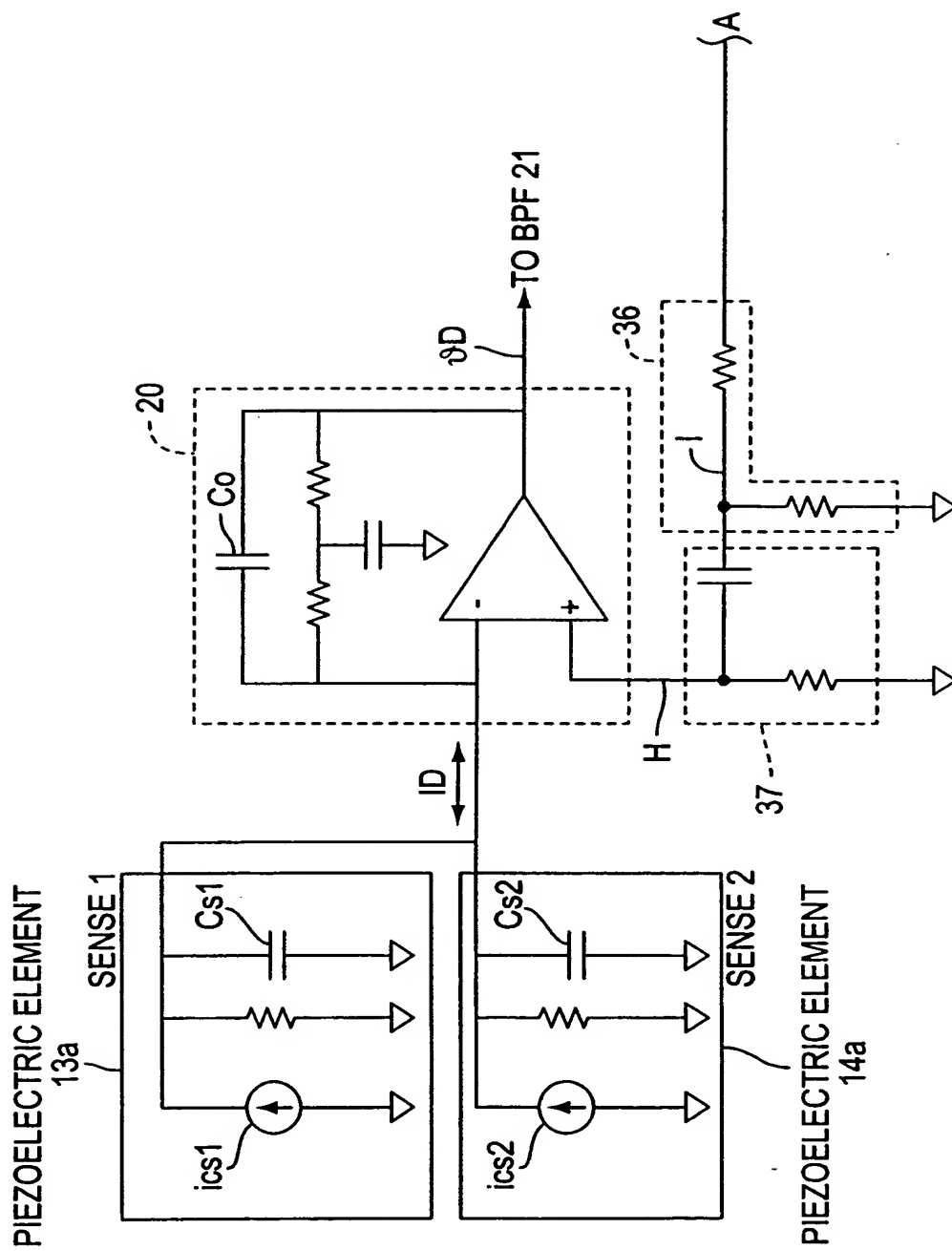


FIG. 13

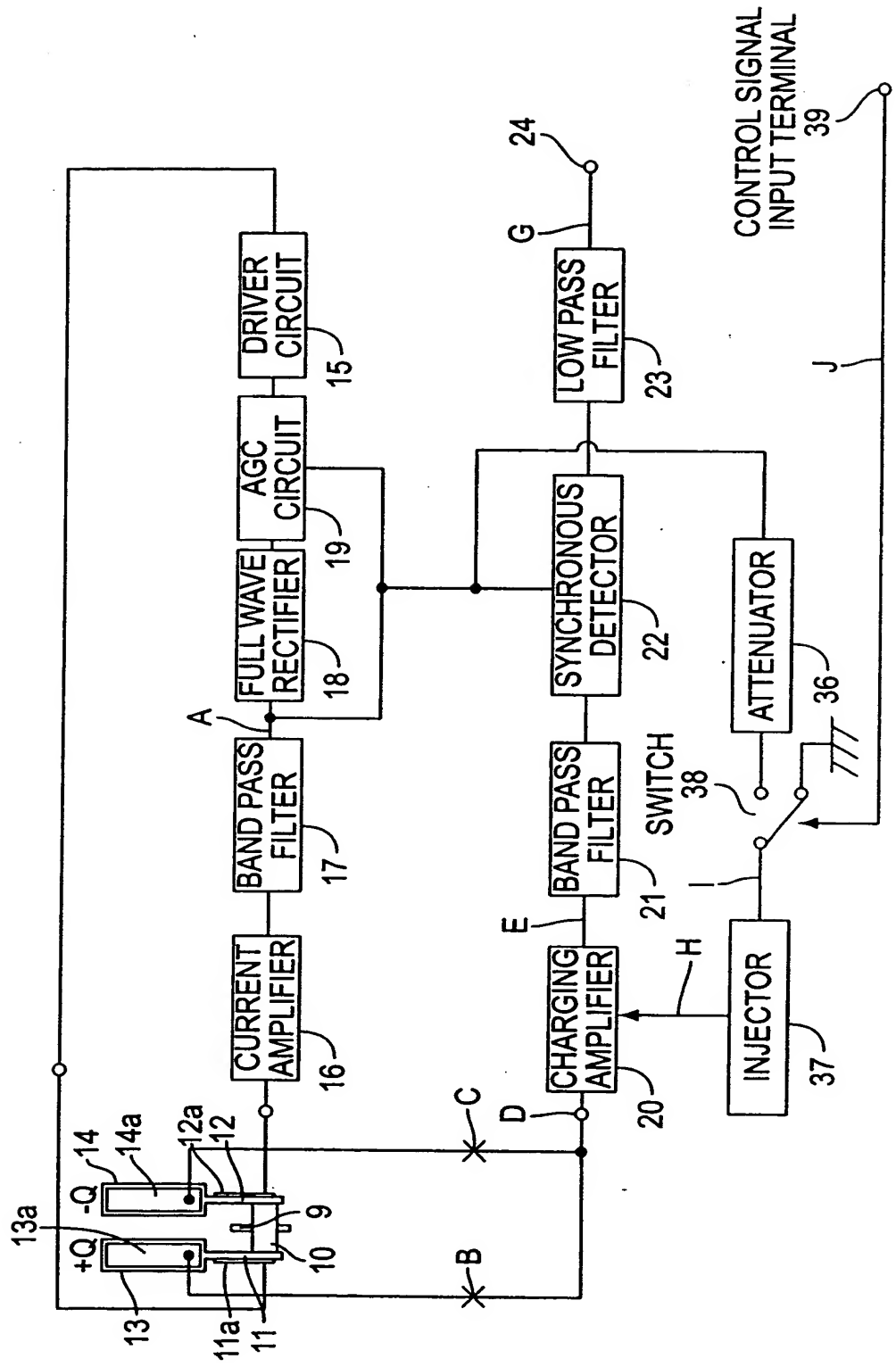


FIG. 14

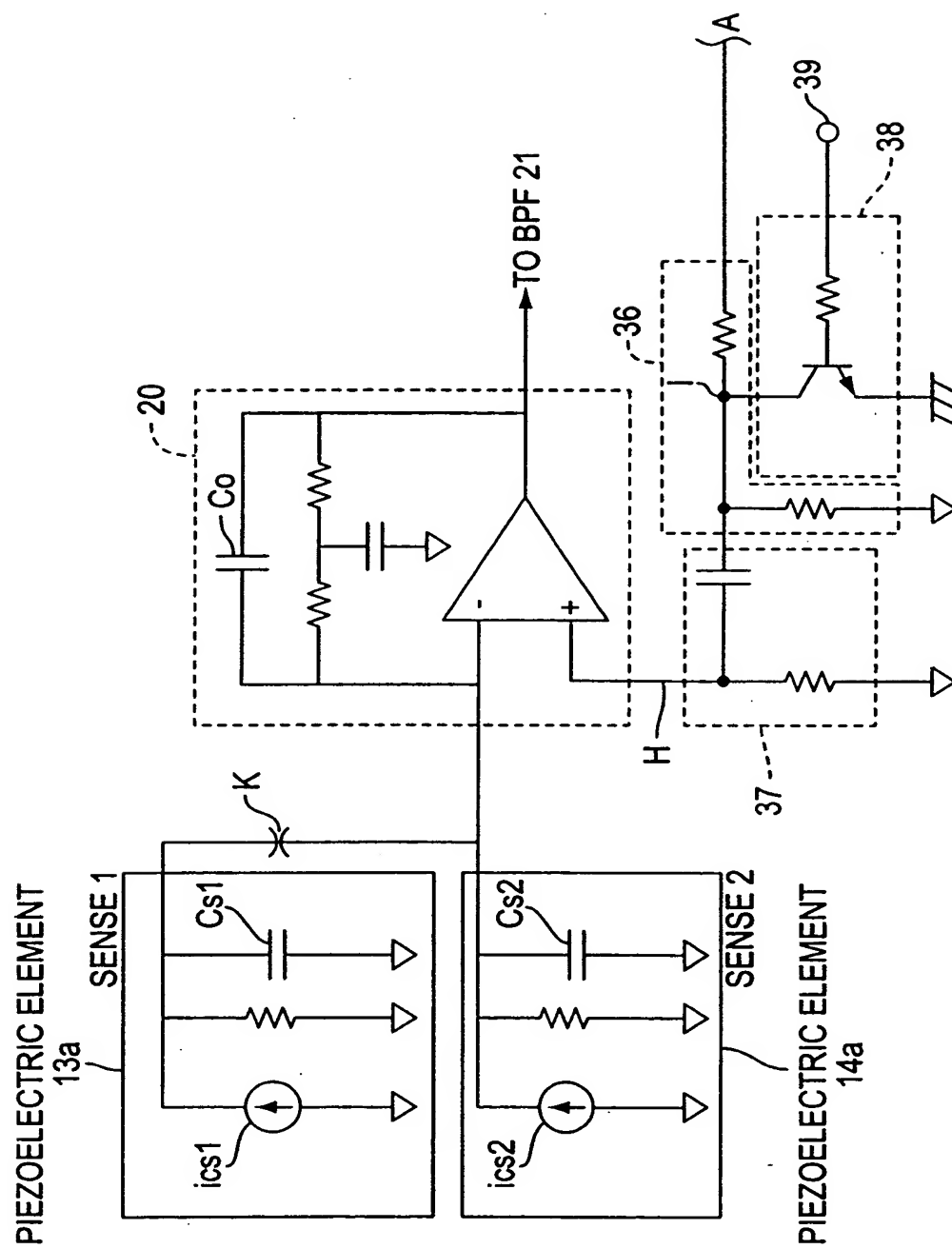


FIG. 15

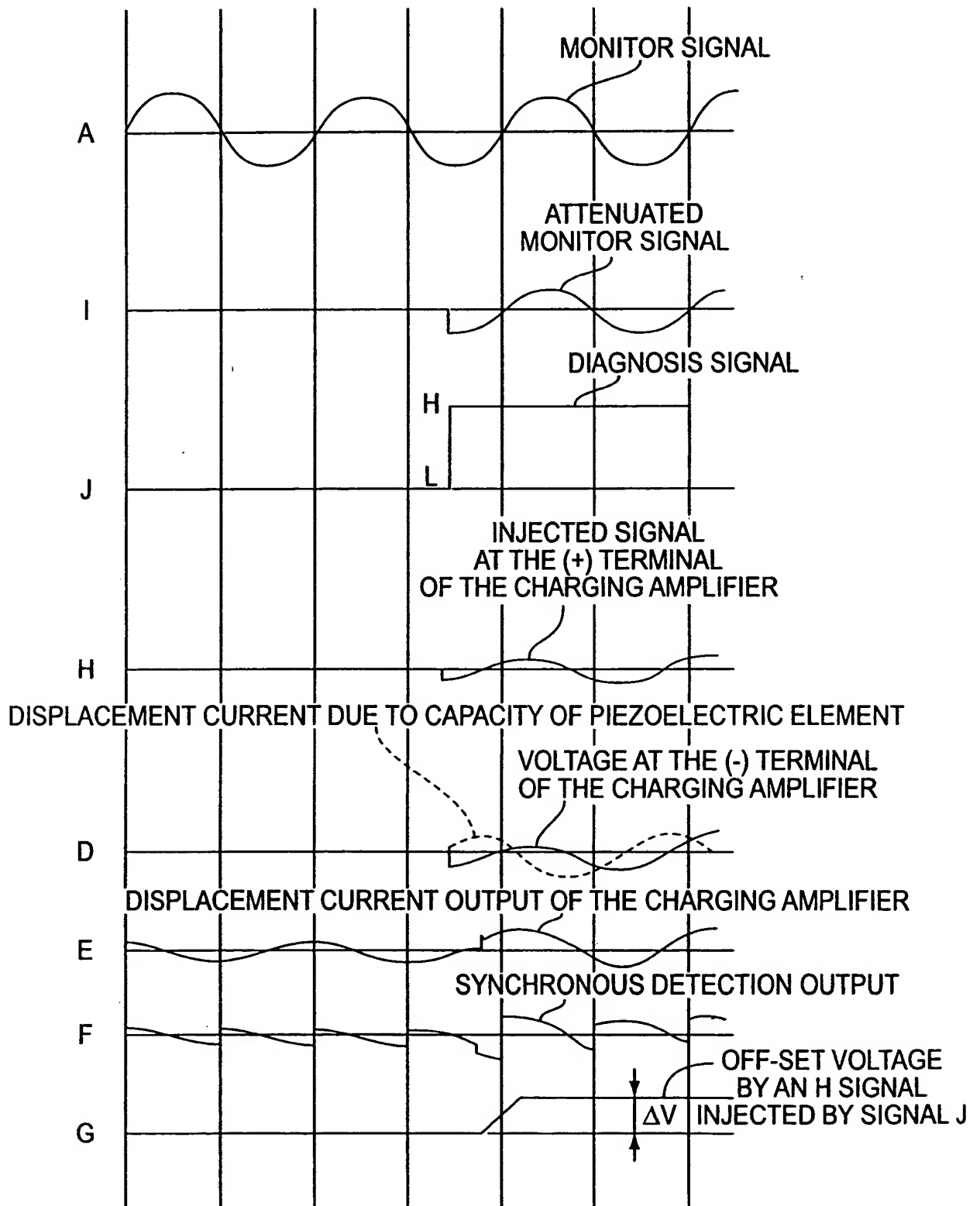


FIG. 16

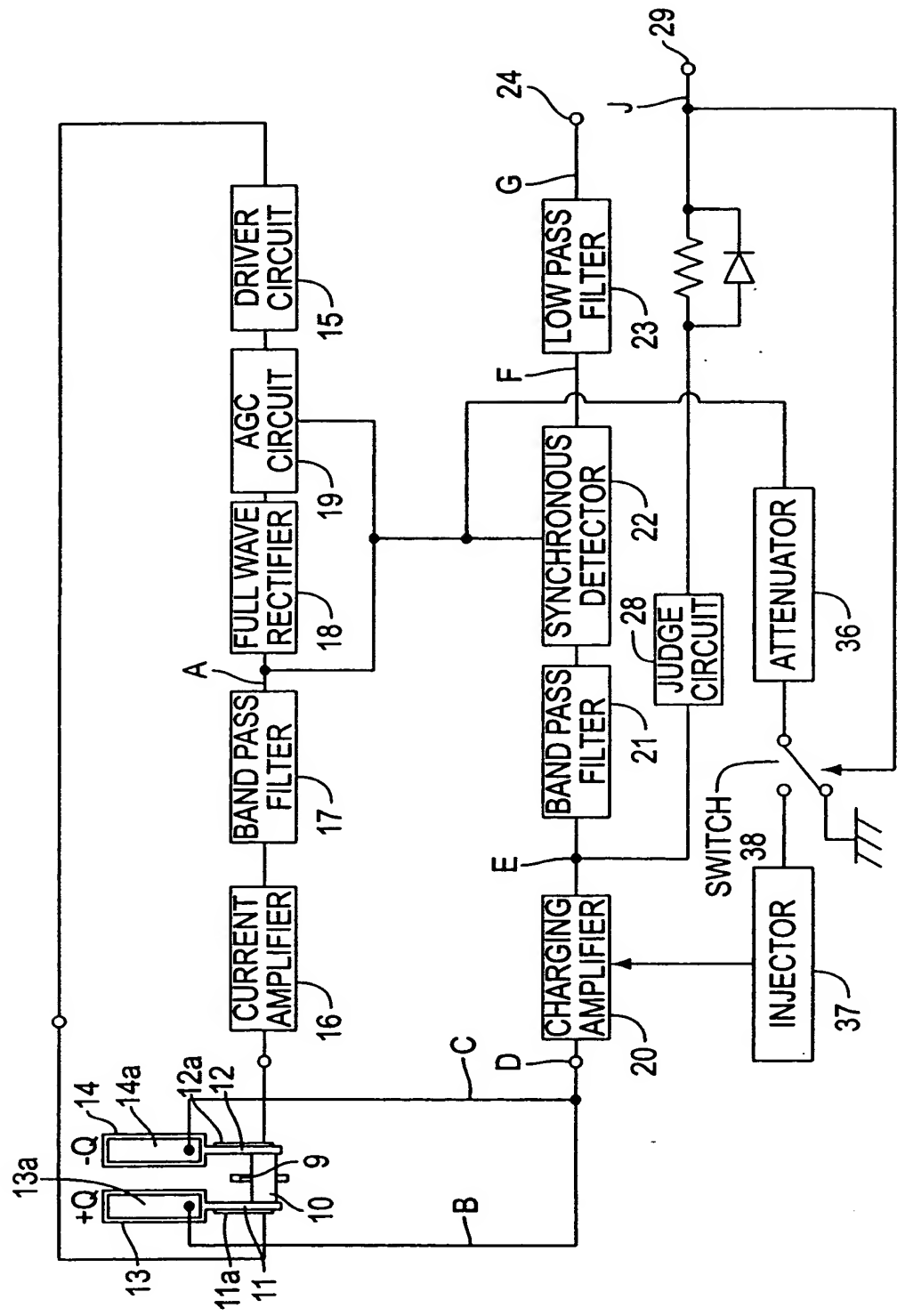


FIG. 17

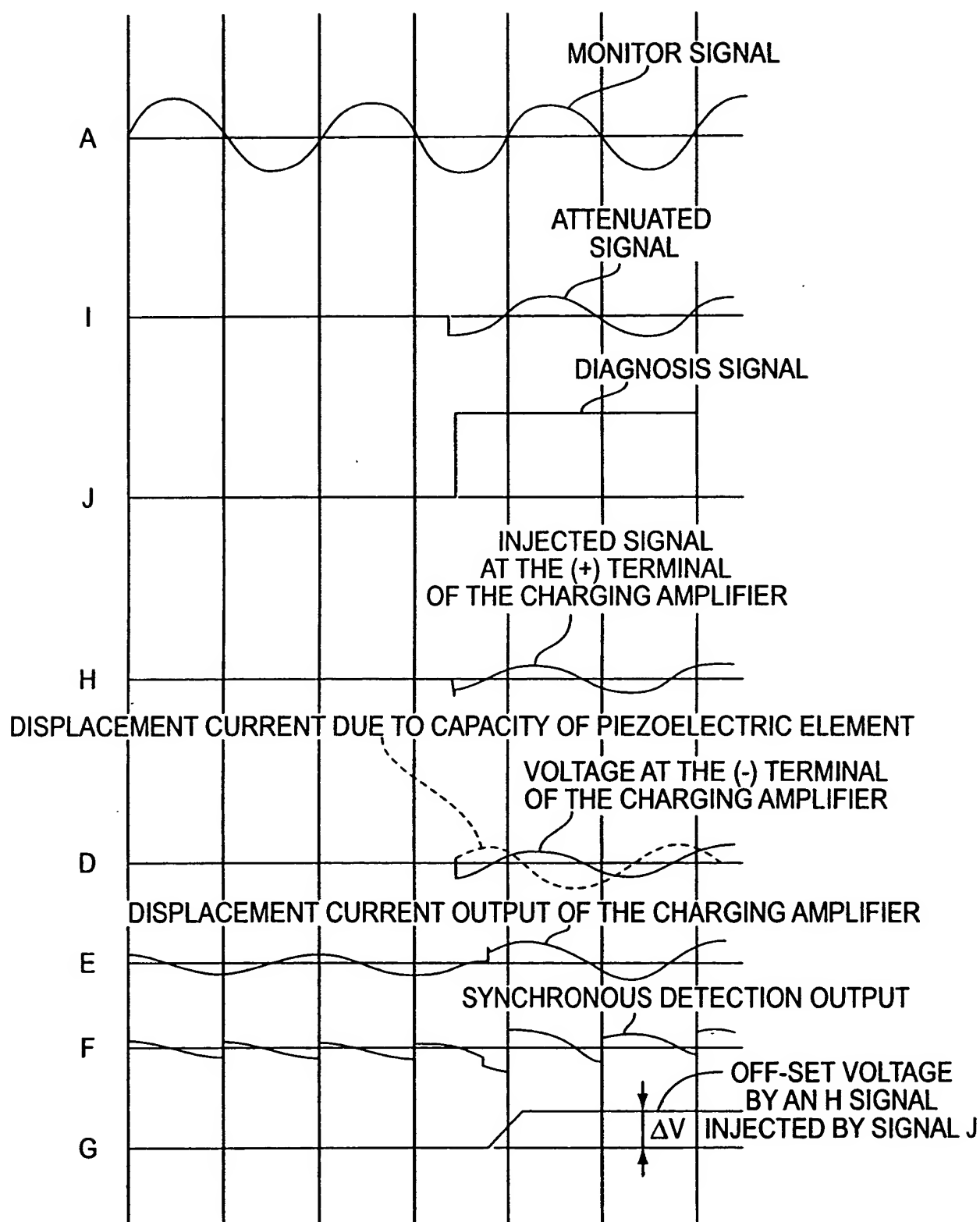


FIG. 18
PRIOR ART

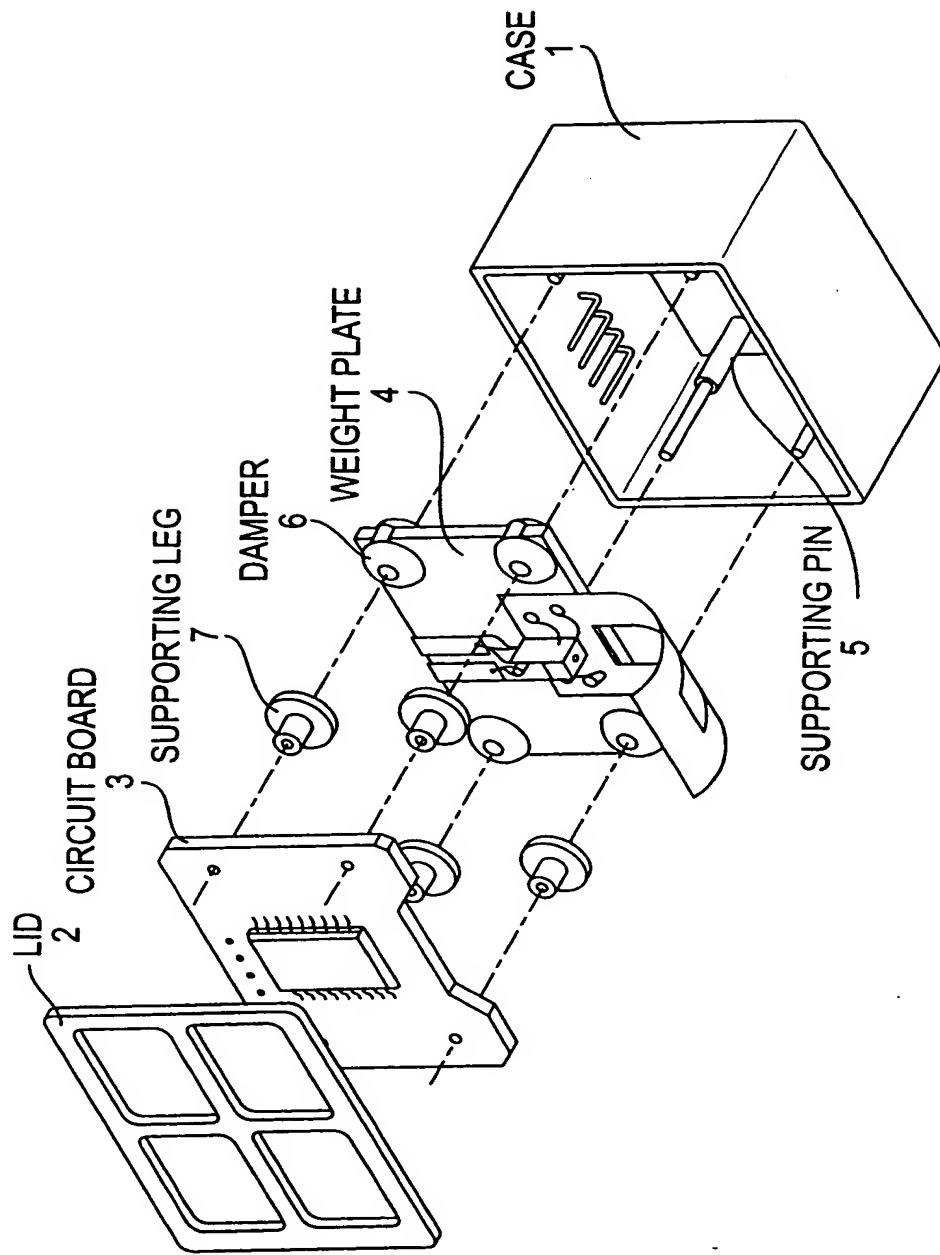


FIG. 19
PRIOR ART

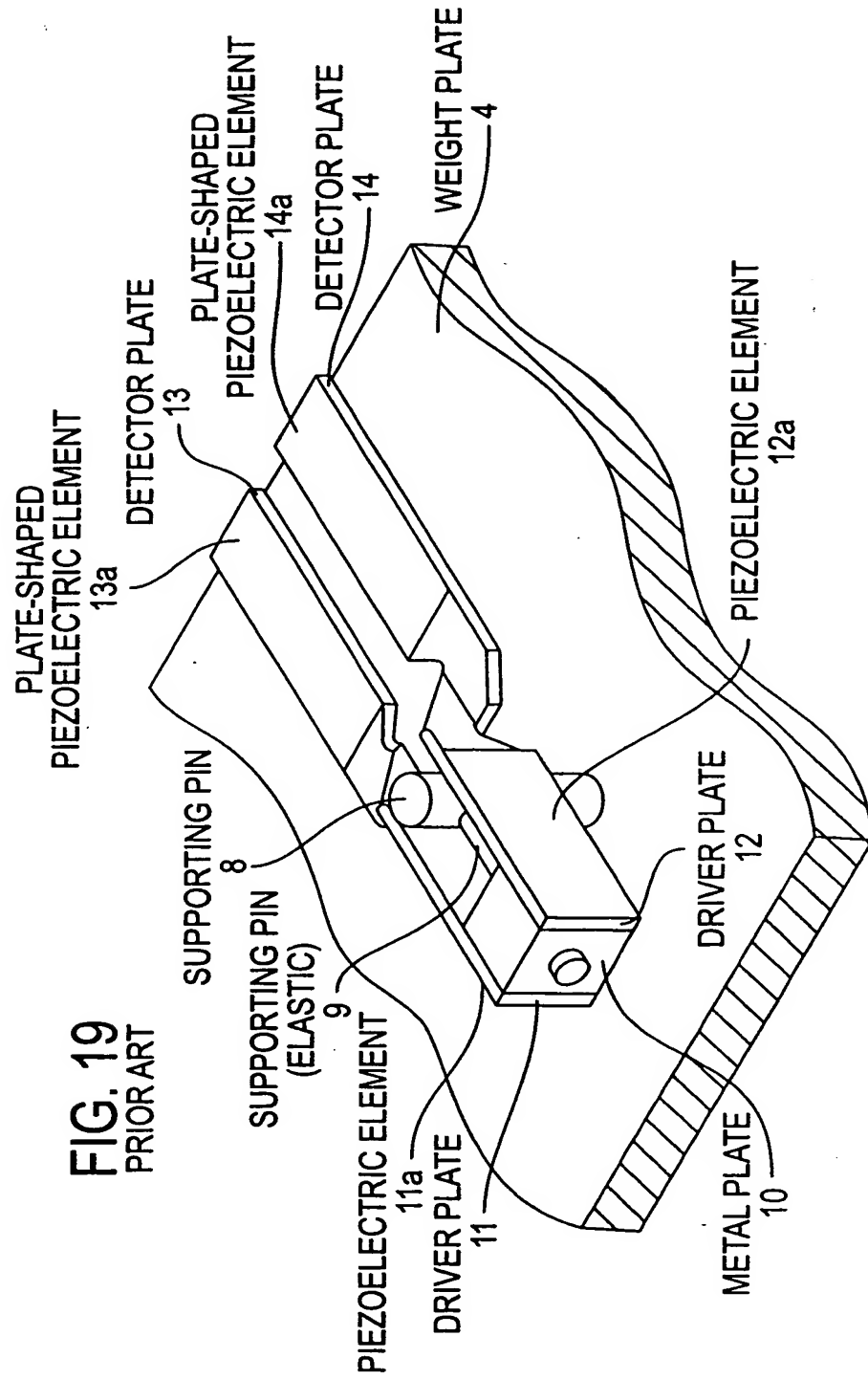
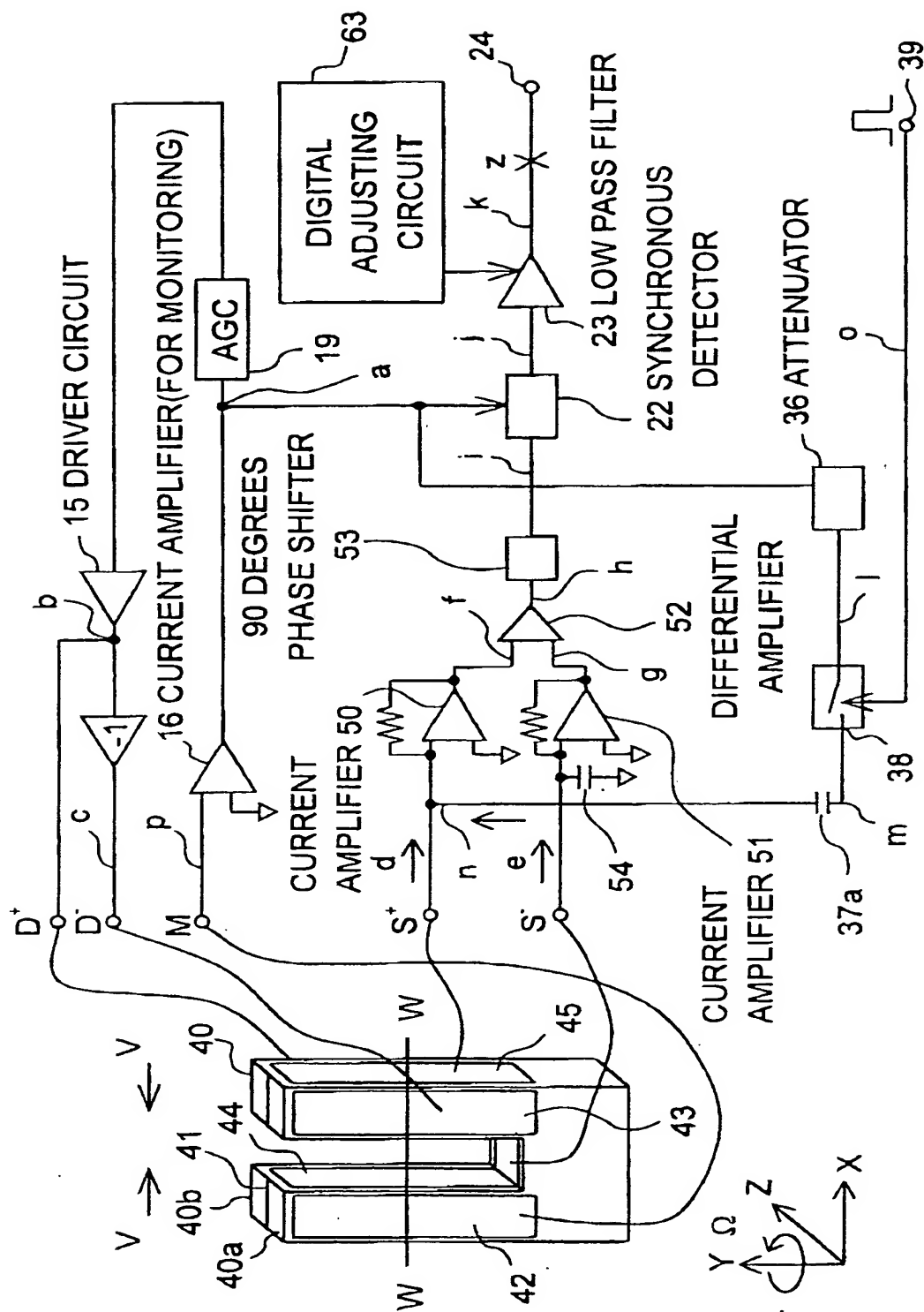


FIG. 20(a)



A schematic diagram of a semiconductor device. It features a central rectangular region labeled S^- and two side rectangular regions labeled S^+ . The top surface of the device is labeled E . The bottom surface is divided into three sections: the left and right sections are labeled D^- and D^+ respectively, and the central section is labeled E . The left and right side regions are labeled $40a$ and $40b$ respectively. The central region is labeled 42 . The top surface of the side regions is labeled 45 . The bottom surface of the side regions is labeled 41 . The top surface of the central region is labeled 46 . The bottom surface of the central region is labeled 44 . The left and right side regions are labeled 43 and 47 respectively. Arrows indicate the direction of current flow: from the S^+ regions towards the S^- region, and from the S^- region towards the S^+ regions.

FIG. 21

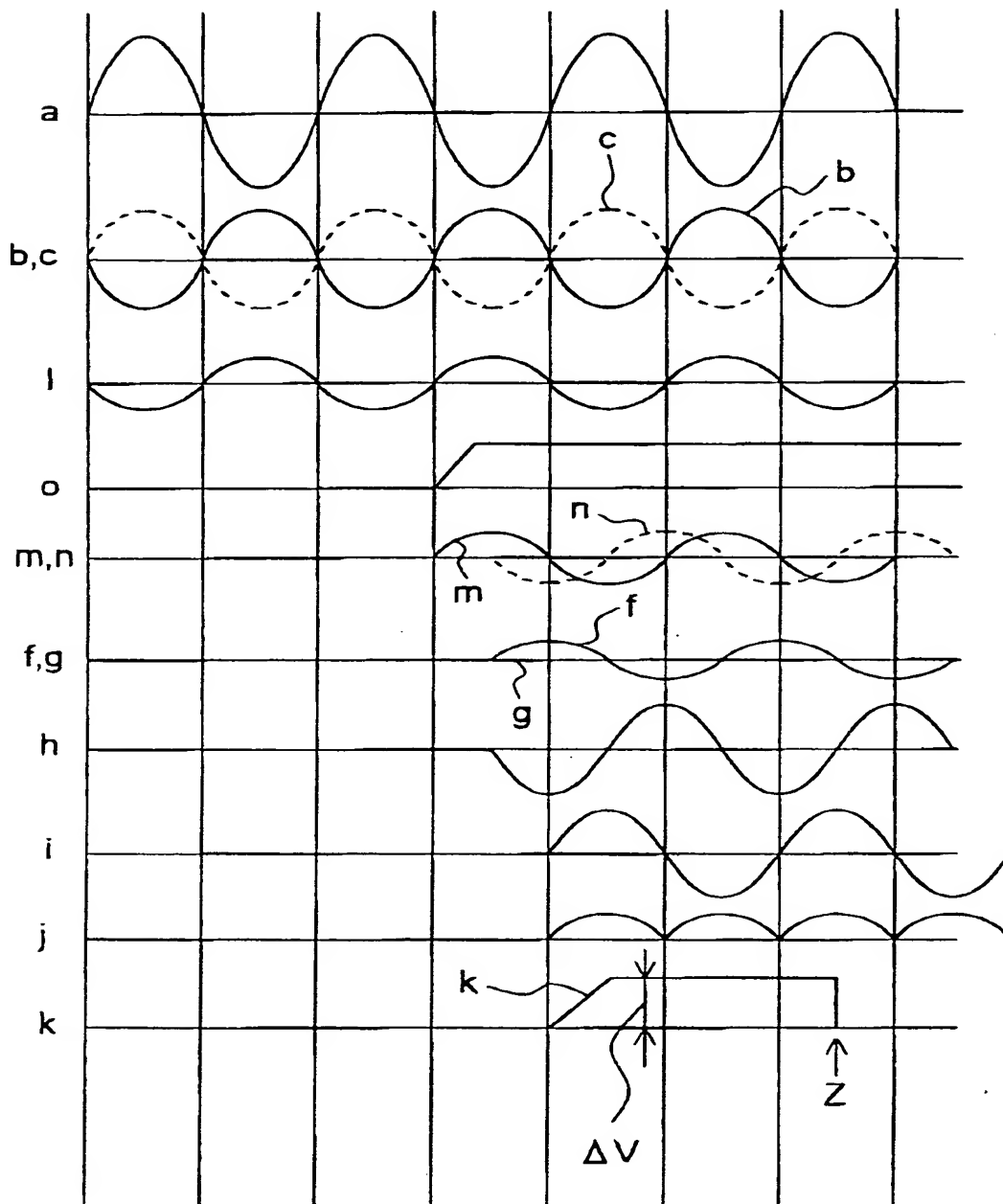


FIG. 22

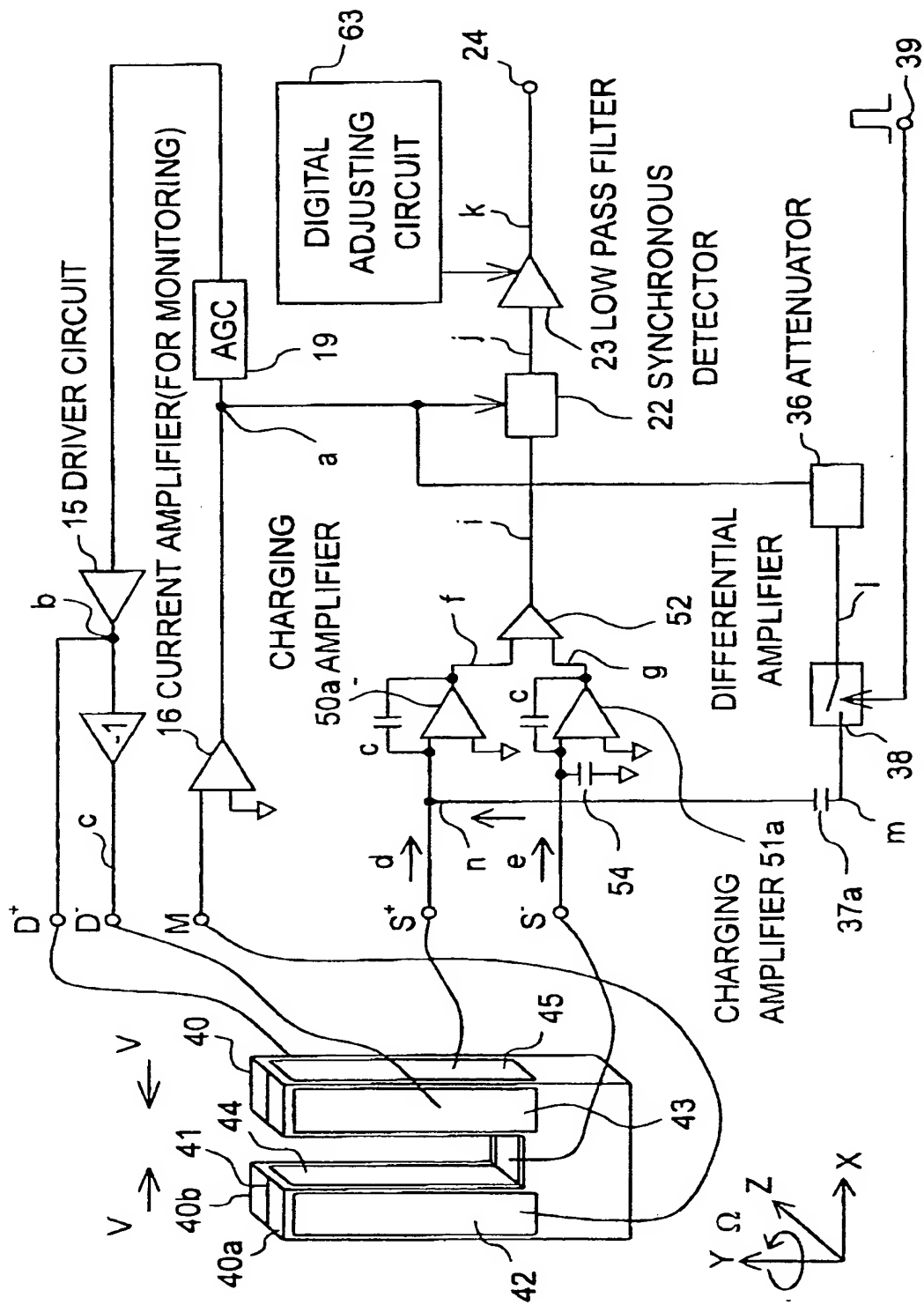


FIG. 23

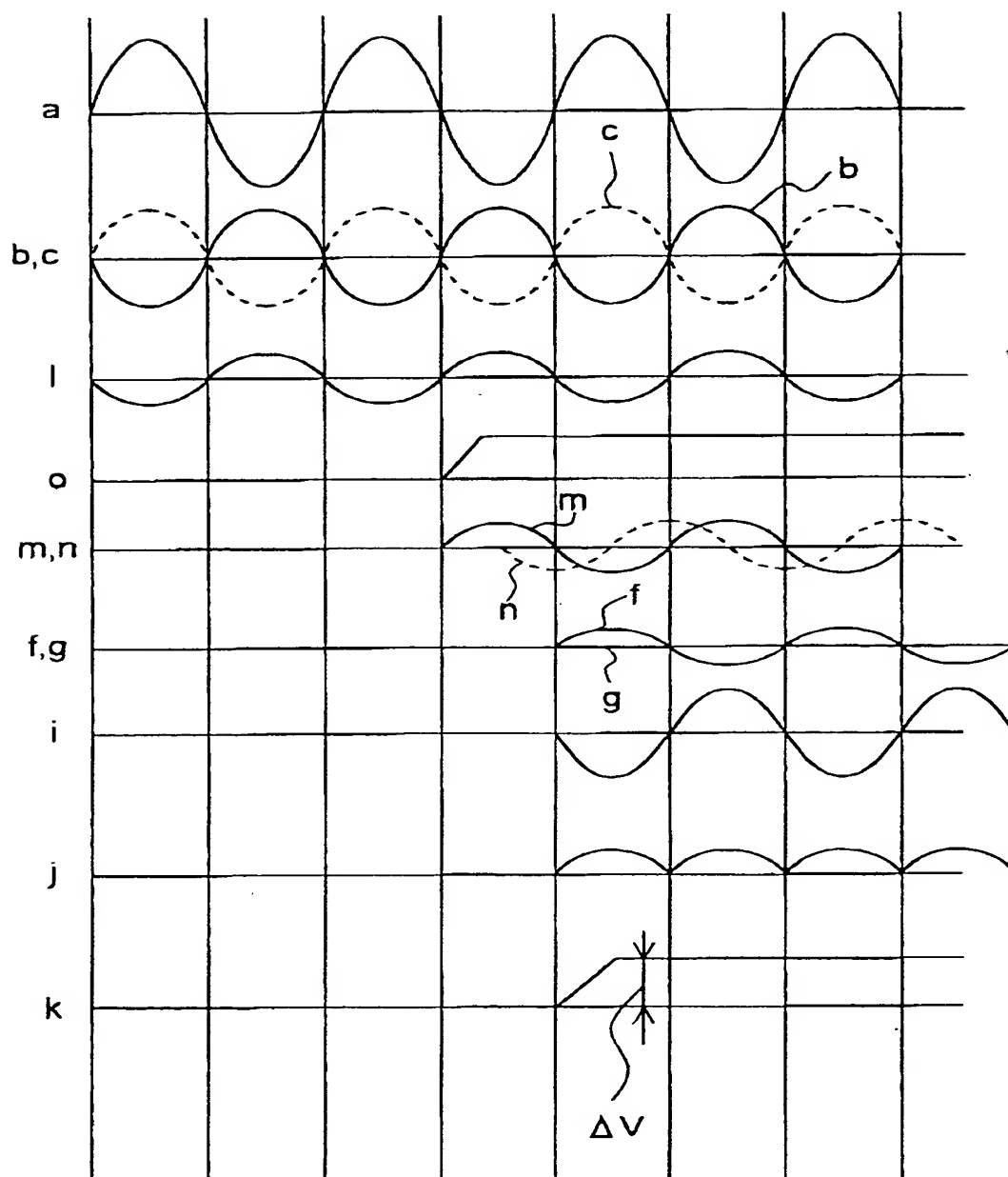


FIG. 24

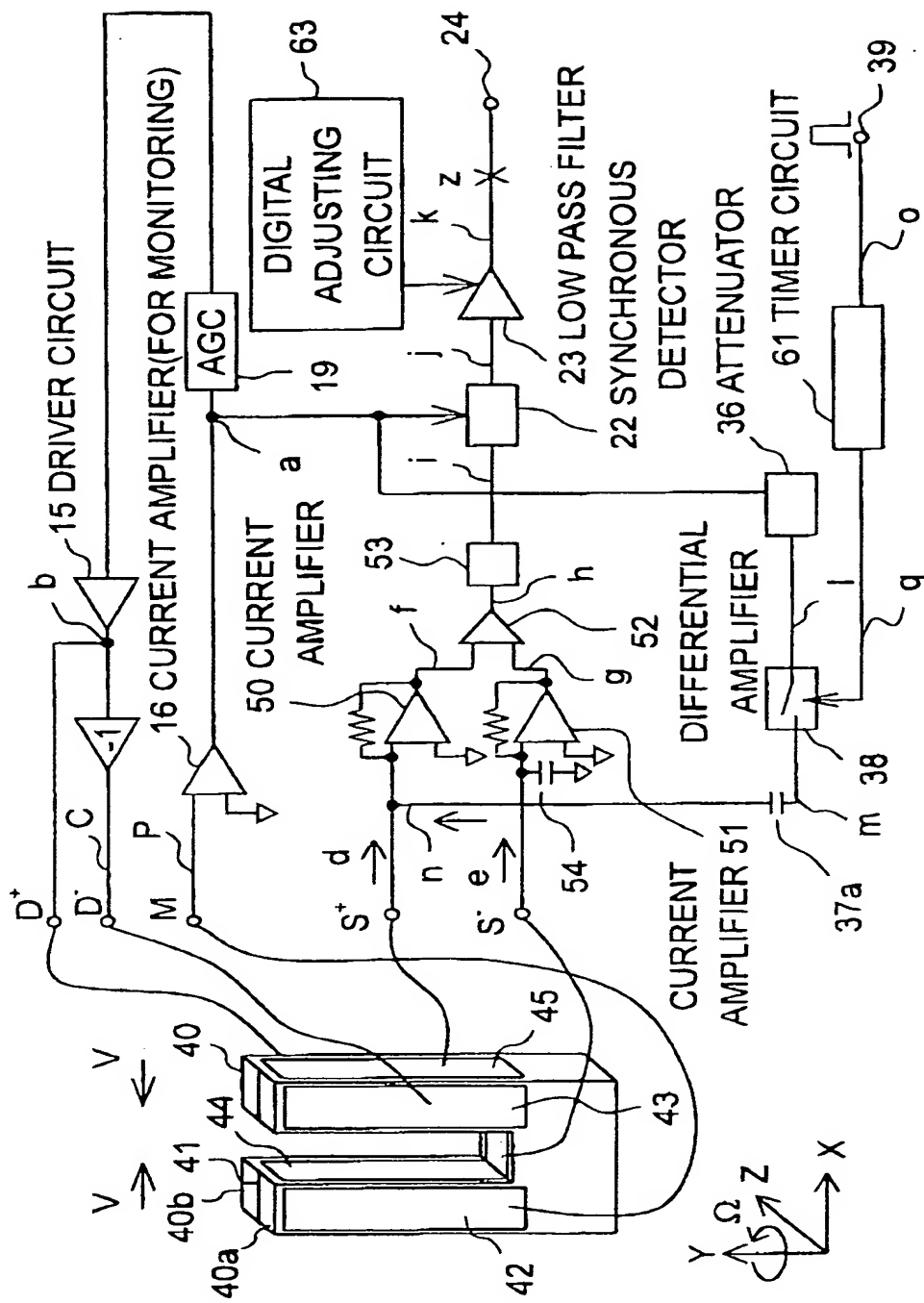


FIG. 25

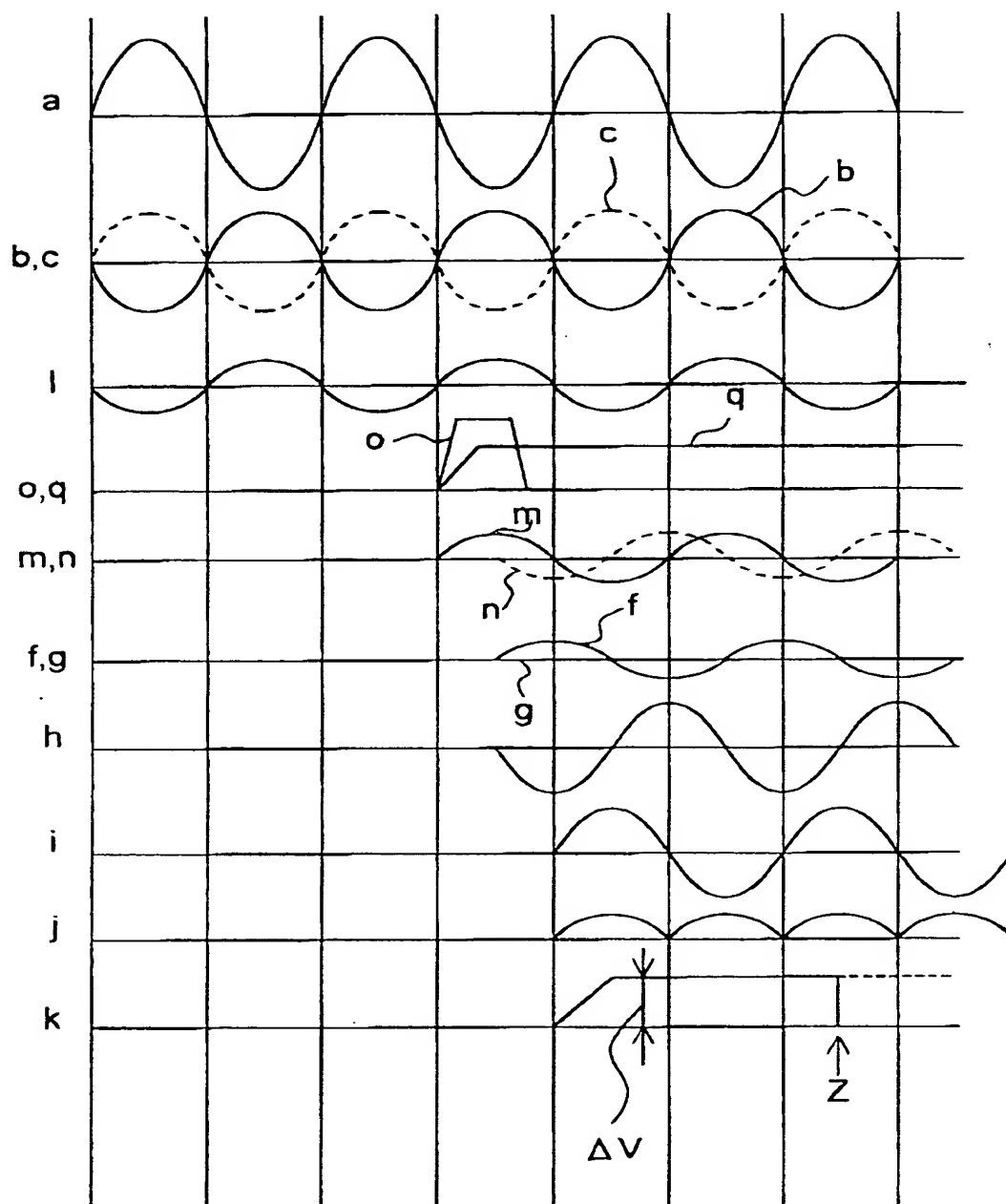


FIG. 26(a)

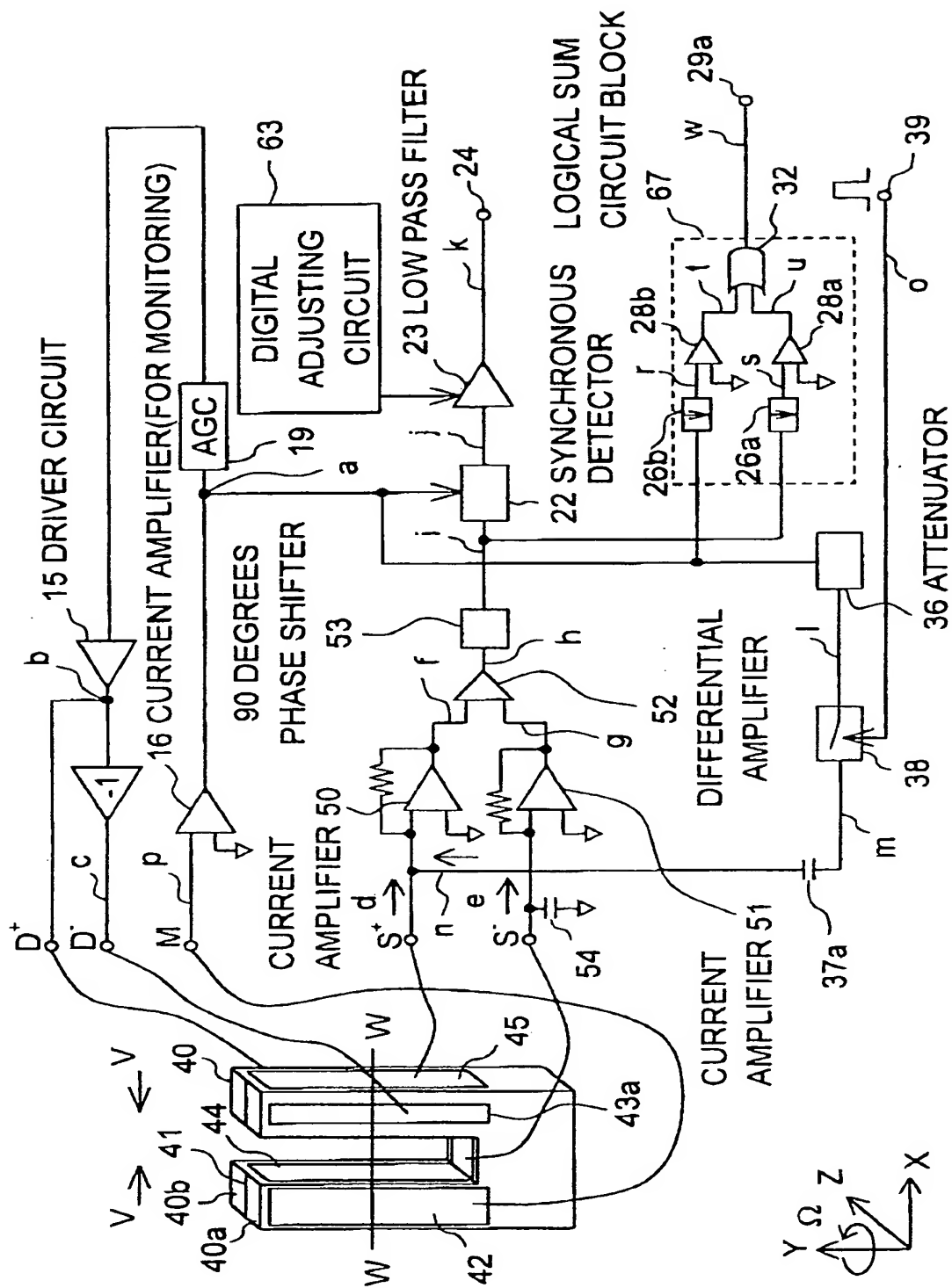


FIG. 26(b)

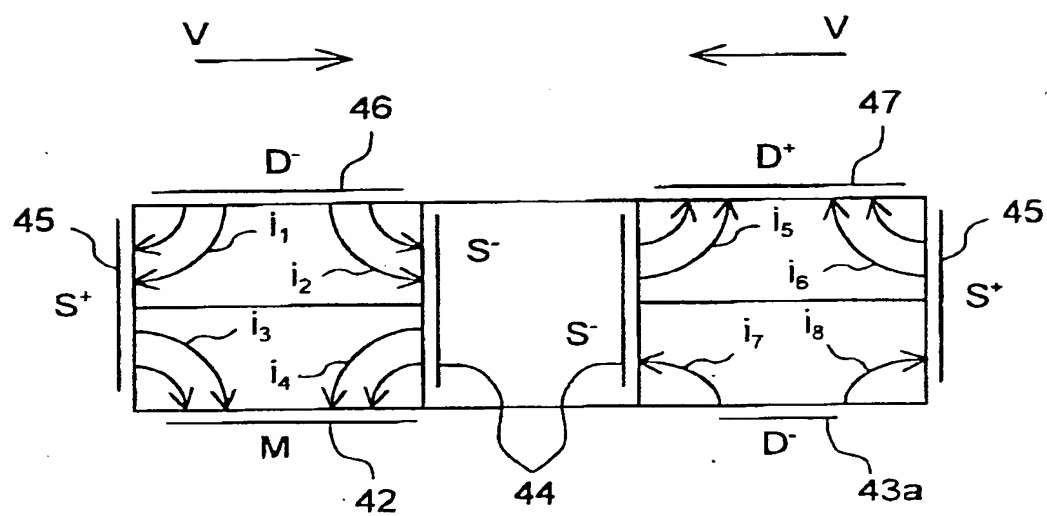


FIG. 27

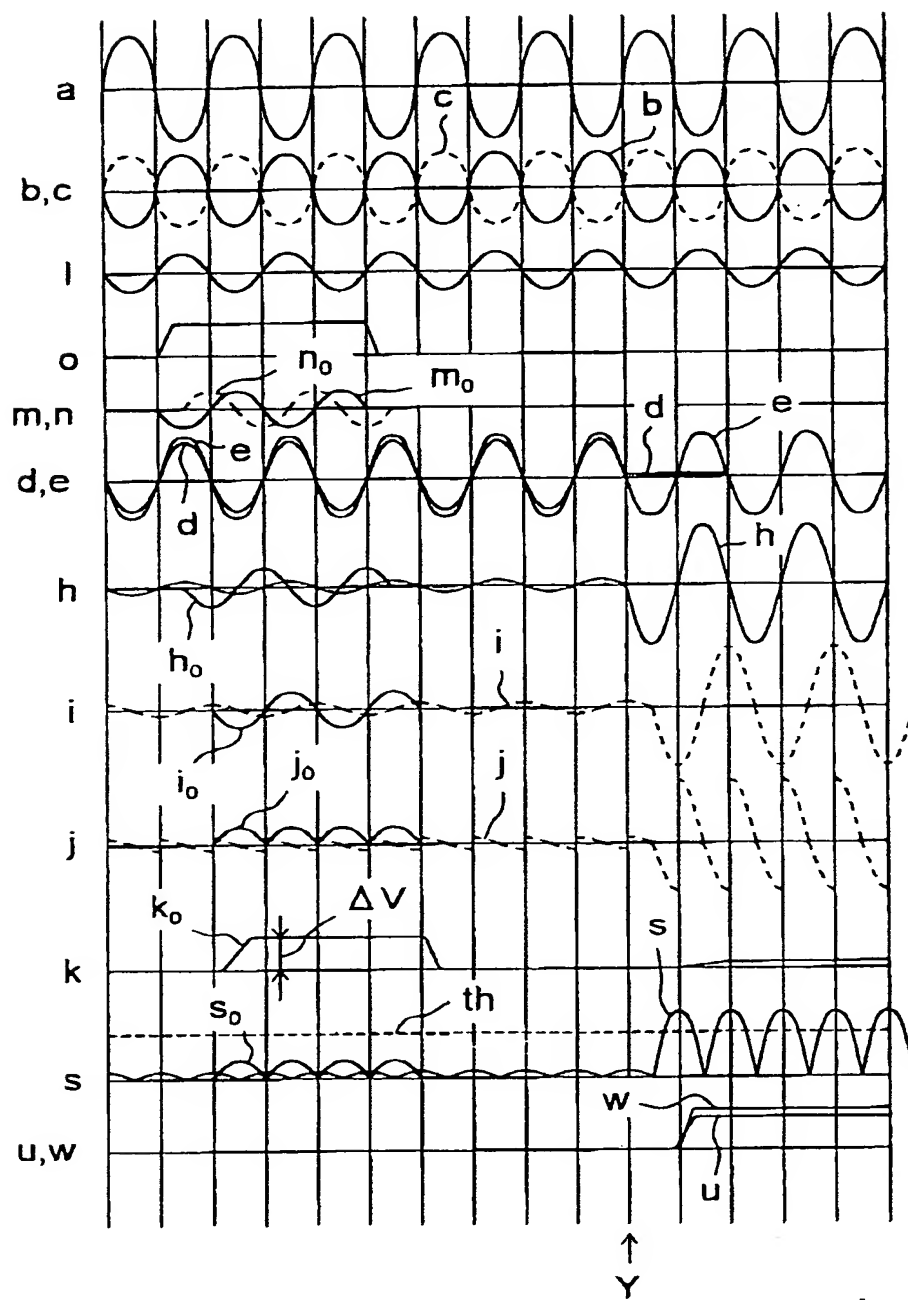


FIG. 28(a)

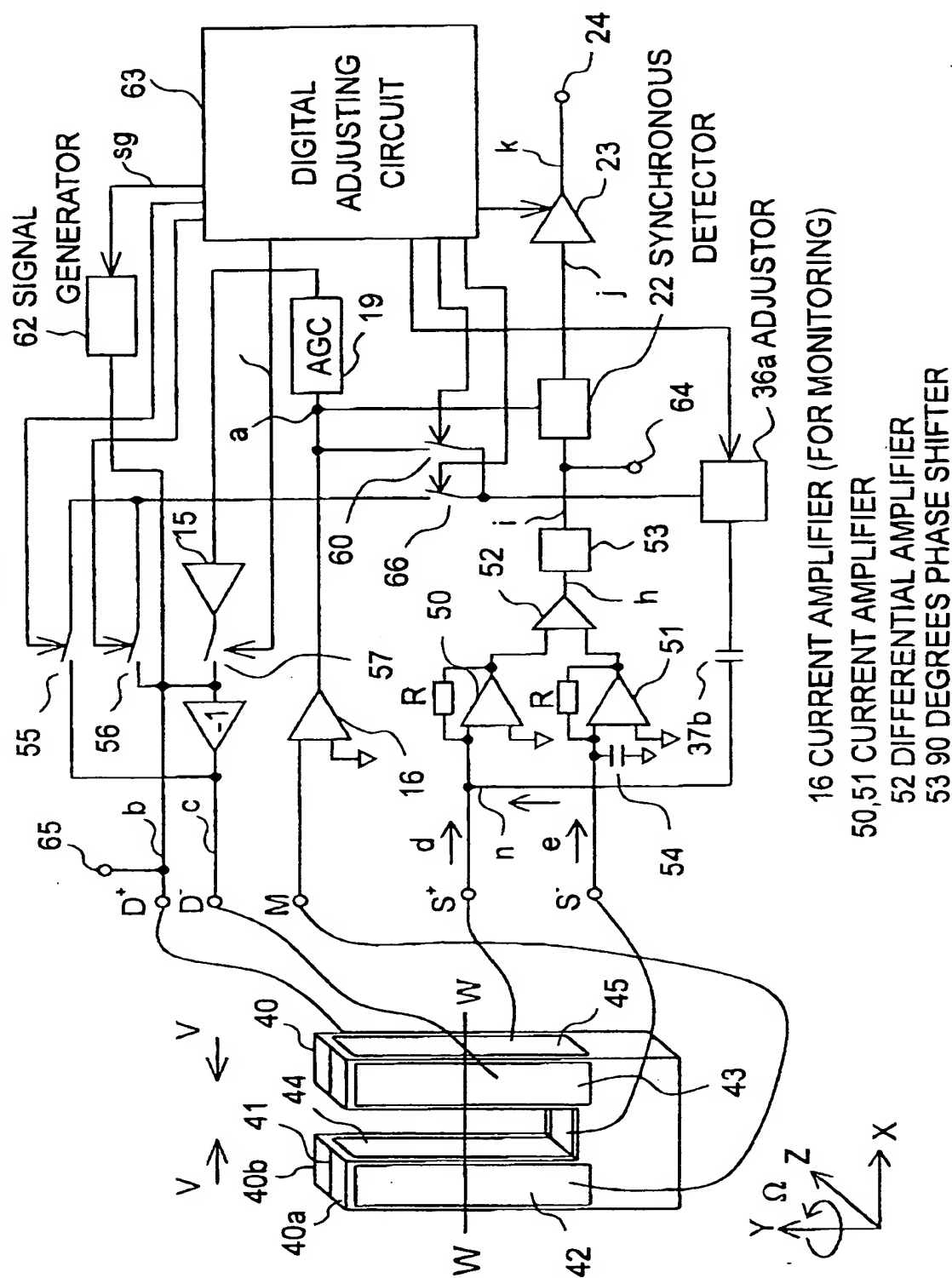


FIG. 28(b)

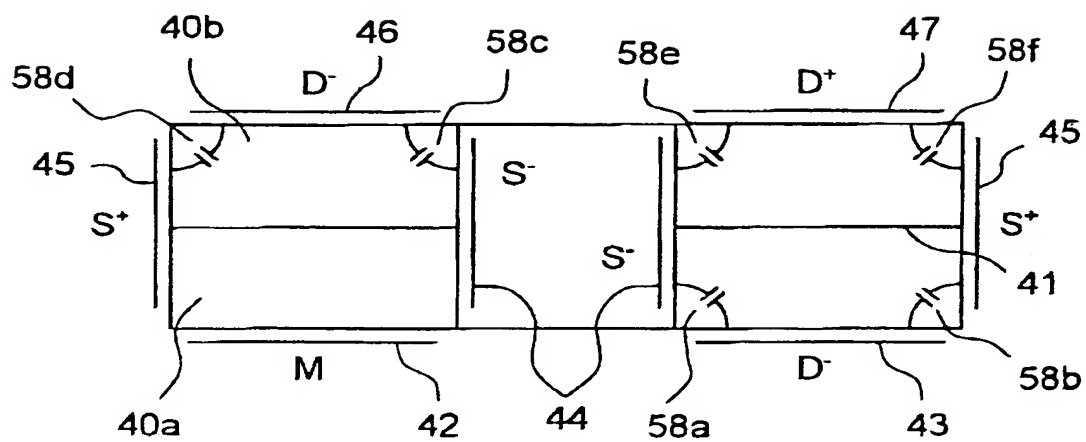
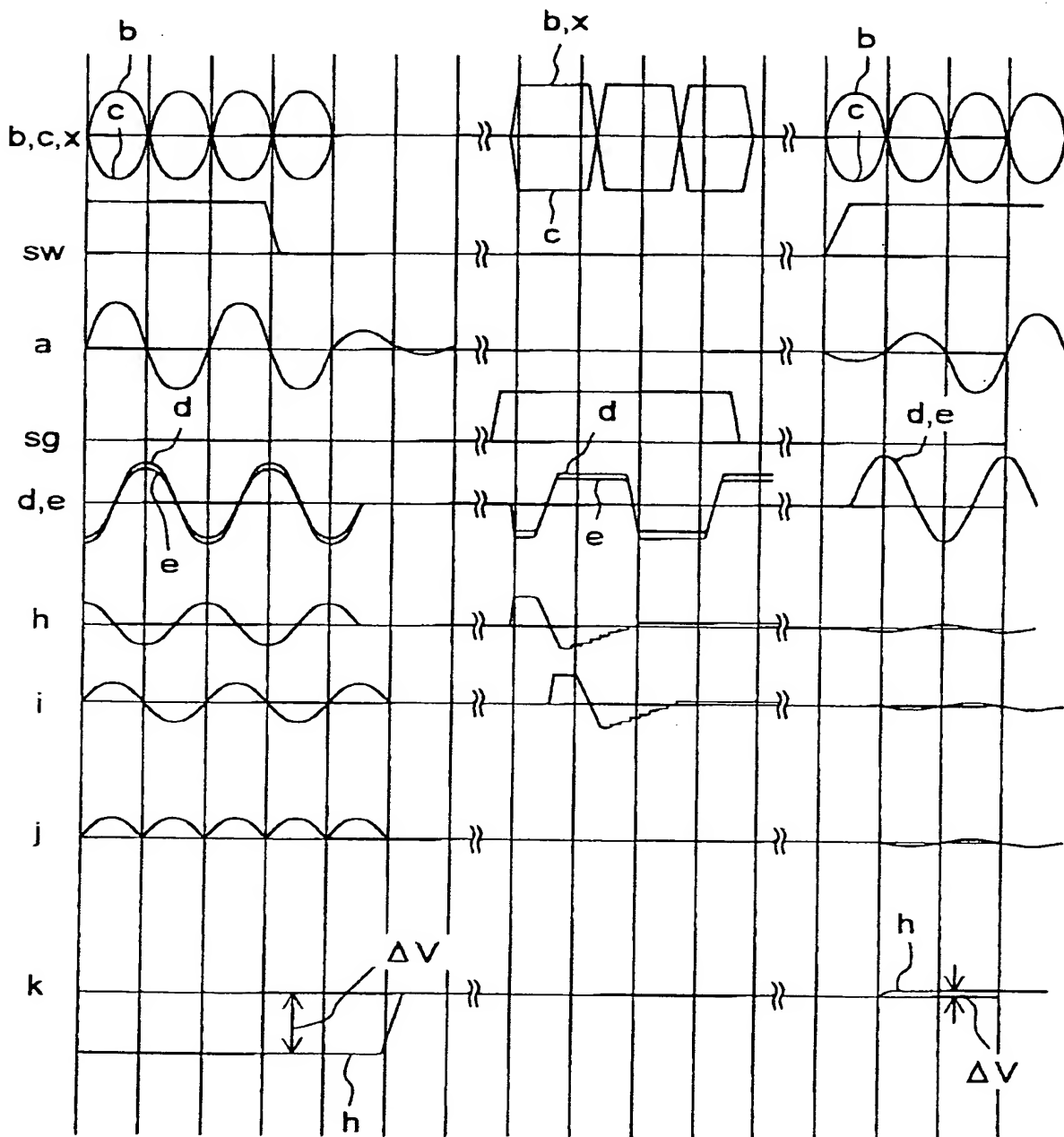


FIG. 29(a)

FIG. 29(b)

FIG. 29(c)



15 DRIVER CIRCUIT

16 CURRENT AMPLIFIER (FOR MONITORING)

19 AGC

23 LOW PASS FILTER

24 69 SYNCHRONOUS DEMODULATOR

36 ATTENUATOR

39 50,51 CURRENT AMPLIFIER

63 DIGITAL ADJUSTING CIRCUIT

40a, 40b, 41, 42, 43, 44, 45

V, Vx, Vy, Vz

Y, Z, X, Ω

50, 51, 54, 55

S⁺, S⁻, n, f, g

37, 38, m